B.TECH(ECE), SEM III, 2014 Digital Circuit and Logic Design PAPER CODE: BTEC 302

PAPER ID:[A1131]

Time: 03 Hours

Maximum Marks: 60

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- 1. Section-A is compulsory, containing of Ten questions carrying Two marks each.
- 2. Section-B contains Five questions carrying Five marks each and student has to attempt any Four questions.
- 3. Section-C contains Three questions carrying Ten marks each and student has to attempt any Two questions.

Section - A

OI.

O3.

- Convert (472.325) 8 to decimal and binary. i.
- ii. Generate a3-bit Gray code.
- Determine logic required to decode binary number (1011)₂ by giving a high indication on output. iii.
- When and where do we use Open-collector operation? iv.
- Determine conversion time of Successive-approximation ADC for a 12-bit resolution at 2.5MHz. V,
- Draw and explain the circuit diagram and truth table for a SR flip-flop. vi.
- What is the most widely used method of representing binary numbers and performing arithmetic vii. in computer systems?
- Simplify and implement the expression using Boolean algebra: viii.
 - i. Y = A'B'C + BC' + A'BC + ABC'
- How the information is transferred into and out of a register? ix.
- Define a Gate and discuss its types. Convert NAND to NOR gate. Х.

Section - B

- Minimize and implement four-variable switching function using Q-M tabulation method: Q2.
 - $F(A, B, C, D) = \sum_{i=1}^{\infty} (0, 1, 2, 3, 4, 6, 8, 9, 10, 11)$
- Differentiate between serial loading and parallel loading for a shift-register. **J**4.
- Describe how the problem of variable load is overcome by using Totem-pole arrangement?
- Q5. Design a 3-bit binary-to-octal decoder.
- Explain the operation of a Parallel-encoded Analog to Digital converter. Q6.

Section - C

- Design a Down counter, that counts in the sequence of 6-5-4-3-2-6-5-4-3-2-6-5, and so on using O7. JK flip-flop.
- Q8: How will you convert a D flip-flop into a JK flip-flop. (i)
 - Describe search operation in CAM. (ii)
- Q9. (i) Explain how a programmable ROM is formed.
 - What is the difference between PLA and PAL? Explain PAL in detail. (ii)